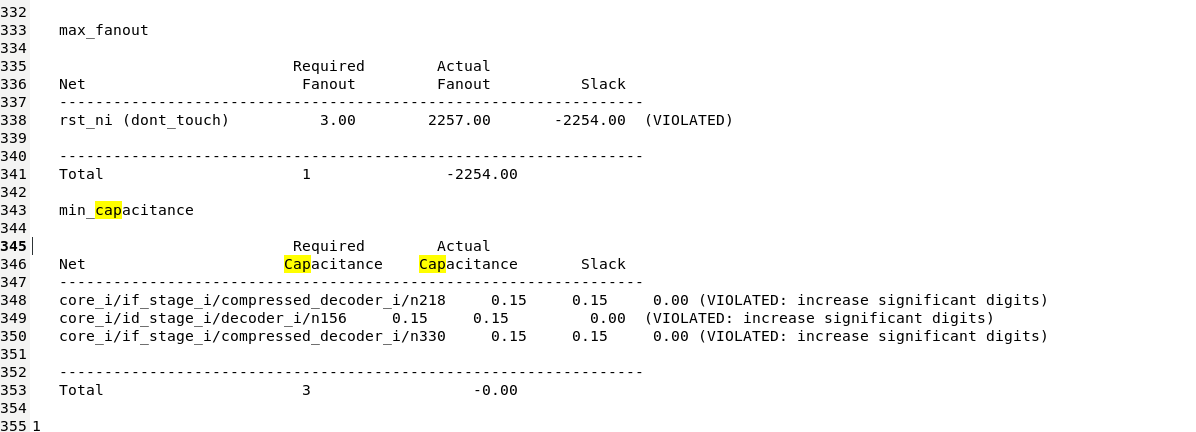
Q1

Synthesis

Why rst\_ni net fanout appeared in the max\_fanout violations list and clk\_i didn’t?

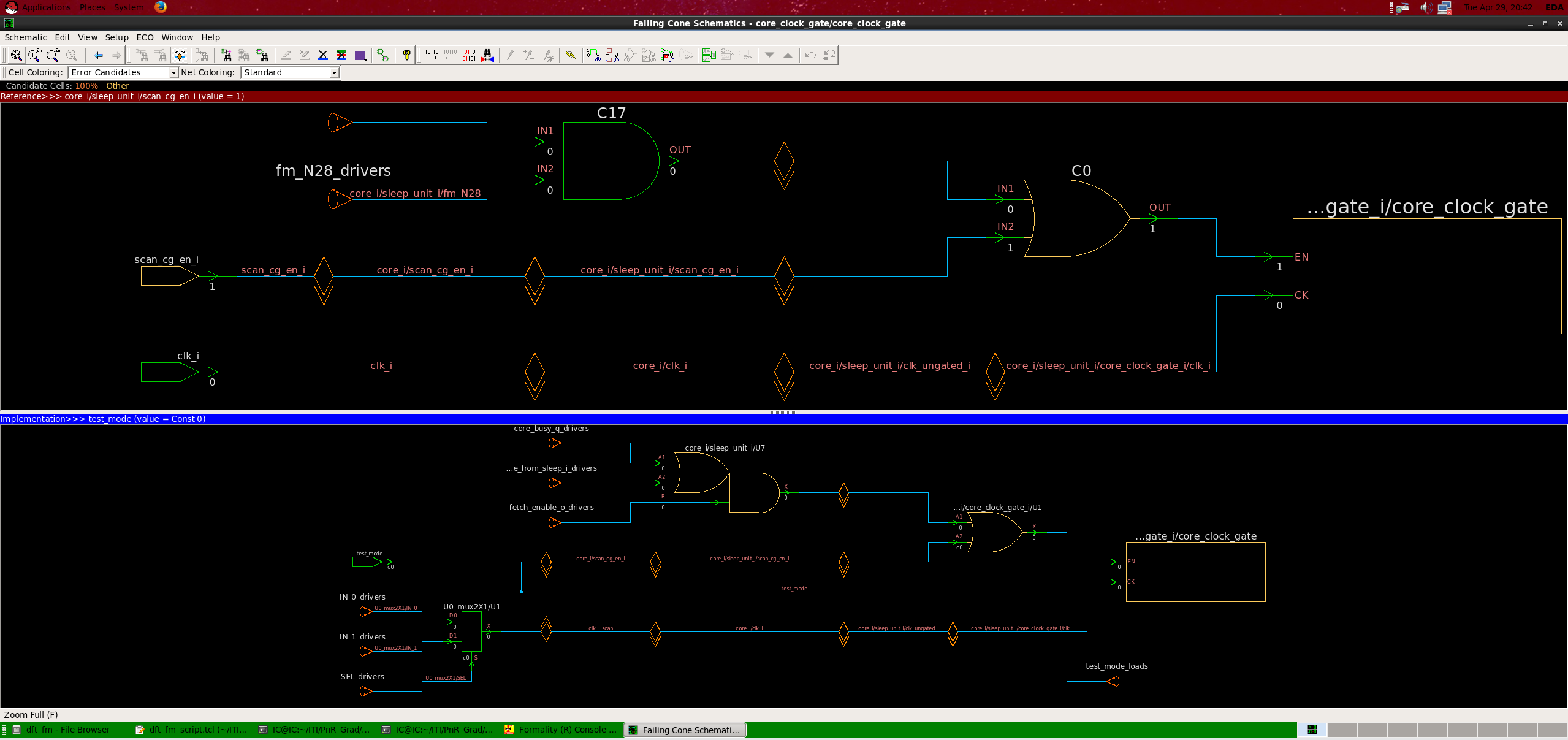


Q2

DFT-FM

Sleep\_unit\_i Failed point

During DFT formal verification, we encountered a mismatch on a net related to scan\_cg\_en\_i and test\_mode. The implementation had test\_mode set to '0' (functional mode), while in the RTL, scan\_cg\_en\_i was set to '1', leading to a comparison failure. However, since this signal was inserted in the original RTL solely for DFT purposes, can it be safely ignored in the equivalence check?



Q3

When I used a list in target\_library "set target\_library [list $SSLIB $TTLIB $FFLIB]" I get an error "can’t' read link library" but if I used only one corner inside the list there is no error so why is this happening?

## Standard Cell libraries

set target\_library [list $SSLIB]

## Standard Cell & Hard Macros libraries

set link\_library [list \* $target\_library]

Q4

Are the following warnings critical? Since these ports should be constrained but cannot be found

